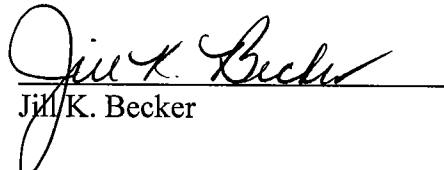


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rich et al. Confirmation No.: 6046
Serial No.: 10/753,852 Group Art Unit: 2825
Filed: 01/08/2004 Examiner: Vuthe Siek
Title: SCALABLE LOGIC SELF-TEST CONFIGURATION
FOR MULTIPLE CHIPS

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being transmitted electronically to: Examiner Vuthe Siek, Group Art Unit 2825, United States Patent and Trademark Office, Alexandria, VA 22313-1450, on August 4, 2006.


Jill K. Becker

Date of Signature: August 4, 2006.

To: Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Response To Office Action

Dear Sir:

This Response to Office Action is being submitted in reply to the Office Action mailed March 10, 2006 for the above-referenced patent application. A Petition for a two-month extension of time is filed herewith.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 4 of this paper.

Amendments to the Claims begin on page 5 of this paper.

Remarks begin on page 10 of this paper.